

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L45	1	((storage adj element) capacitor condenser) and (plurality second) and ((match\$ near3 load) or (load near3 match\$)) and (power adj (monitor monitoring)) and demand).clm.	US-PGPUB	OR	ON	2007/01/11 14:18
L46	1	((storage adj element) capacitor condenser) and (plurality second) and ((match\$ near3 load) or (load near3 match\$)) and (power with (monitor monitoring)) and demand).clm.	US-PGPUB	OR	ON	2007/01/11 14:19
L47	1	((storage adj element) capacitor condenser) and (plurality second) and ((match\$ with load)) and (power with (monitor monitoring)) and demand).clm.	US-PGPUB	OR	ON	2007/01/11 14:19
L48	1	((storage adj element) capacitor condenser) and (plurality second) and ((match\$ with load)) and (current with (monitor monitoring)) and demand).clm.	US-PGPUB	OR	ON	2007/01/11 14:19
L49	1	((storage capacitor condenser) and (plurality second) and ((match\$ with load)) and (current with (monitor monitoring)) and demand).clm.	US-PGPUB	OR	ON	2007/01/11 14:19

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	243	(lithium adj polymer adj (battery cell)) and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:18
L4	32	(lithium adj polymer adj (battery cell)).ti. and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:19
L5	0	(power adj (monitor monitor\$)) same (maximum adj current) same (disconnect disconnected) and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:21
L6	4	(power adj (monitor monitor\$)) and (maximum adj current) and (disconnect disconnected) and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:21
L7	2	(power adj (monitor monitor\$)) same battery same current same (disconnect disconnected) and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:22
L8	248	((cell battery) adj (monitor monitoring control controlling controller)).ti. and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:22
L9	4	((cell battery) adj (monitor monitoring control controlling controller)).ti. and (current same (disconnect disconnect\$)) and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:24

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L10	0	((cell battery) adj (monitor monitoring control controlling controller)).ti. and ((current near2 maximum) same (disconnect disconnect\$)) and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:23
L11	1443	(power adj (monitor monitoring control controlling controller)).ti. and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:24
L12	1	(power adj (monitor monitoring control controlling controller)).ti. and ((current near2 maximum) same (disconnect disconnect\$)) and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:25
L13	0	(shunt adj controller) same resistor same (switch transistor) and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:26
L14	1	(shunt adj controller) same (resistor resistance) same (switch transistor) and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:27
L15	336	(bypass by-pass) same (resistor resistance) same (switch transistor) and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:28
L16	11	(source near2 (bypass by-pass)) same (resistor resistance) same (switch transistor) and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:31

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L17	4	((voltage adj source) near2 (bypass by-pass)) same (resistor resistance) same (switch transistor) and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:32
L18	36	shunt same ((threshold maximum) adj voltage) same (resistor resistance) and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:33
L19	20	shunt same ((threshold maximum) adj voltage) same (resistor resistance) same (switch transistor) and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:33
L20	0	shunt same ((threshold maximum) adj voltage) same (resistor resistance) same (switch transistor) and (shunt shunt\$.ti. and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:34
L21	88	(shunt adj resistor) same switch and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:34
L22	22	(shunt adj resistor) same switch and (threshold adj voltage) and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:34
L23	0	"361"/\$.ccis. and ((shunt adj controller) same resistor same (threshold adj voltage)) and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:35

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L24	16	307/140.ccls. and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:35
L25	3	(power adj supply) same (supercapacitor or (super adj capacitor) or super-capacitor) same inductor same battery and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:39
L26	9	307/52.ccls. and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:41
L27	1	307/59.ccls. and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:41
L28	8	307/109.ccls. and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:43
L29	0	"320"/\$.ccls. and (supercapacitor or super-capacitor or (super adj capacitor)) and (inductor inductance) and ((capacitor adj bank) or (bank near2 capacitor)) and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:44
L30	0	(power adj supply) and (lithium adj polymen) and inductor and (electrolytic adj capacitor) and (supercapacitor or super-capacitor or (super adj capacitor)) and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:44

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L31	238	battery same (supercapacitor or super-capacitor or (super adj capacitor)) same (electrolytic capacitor) and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 13:35
L32	64	bedard.in. and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:46
L33	8	bedard-stephane.in. and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 11:47
L34	5	battery same (supercapacitor or super-capacitor or (super adj capacitor)) same (electrolytic capacitor) same inductor and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 13:35
L35	24	battery same (supercapacitor or super-capacitor or (super adj capacitor)) same (electrolytic capacitor) and inductor and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 13:35
L36	6	(dc adj converter) and (supercapacitor super-capacitor (super adj capacitor)) and inductor and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 13:36
L37	7	converter and (supercapacitor super-capacitor (super adj capacitor)) and inductor and battery and (recharge re-charge) and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 13:37

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L38	0	converter.ti. and (shunt adj controller) and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 13:38
L39	19	converter.ti. and (shunt adj resistor) and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 13:38
L40	0	converter.ti. and ((shunt adj resistor) same ((threshold maximum) adj voltage)) and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 13:38
L41	2	power adj drive adj controller and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 13:39
L42	61	(power adj controller) same ((input source supply) near2 current) same (load neare2 current) and @pd>="20060428"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/11 13:39